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# Towards Real-Time Implementation for the Pre-Processing of Radar-Based Human Activity Recognition

Alexandre Bordat<sup>1,2</sup>, Petr Dobias<sup>1,3</sup>, Julien Le Kernec<sup>1,4</sup>, David Guyard<sup>2</sup>, Olivier Romain<sup>1,4</sup>

<sup>1</sup>*ETIS Laboratory UMR 8051, CY Cergy Paris University, ENSEA, CNRS, F-95000 Cergy, France*

<sup>2</sup>*BlueLinea, 6 Rue Blaise Pascal, 78990 Élanecourt, France*

<sup>3</sup>*ESIEE-IT, 8 rue Pierre de Coubertin, 95300 Pontoise, France*

<sup>4</sup>*James Watt School of Engineering, University of Glasgow, Glasgow, United Kingdom*  
alexandre.bordat@ensea.fr

**Abstract**—The rapid aging of the population combined with the correlation between age and the increase in falls pushes us to create new ways to monitor the elderly. The privacy of radar data can respond to one of the weaknesses of existing technologies, but the huge amount of radar data to process becomes a challenge to process. We therefore introduce a first architecture allowing the processing of its data in real time. The radar technology used is an off-the-shelf Frequency Modulated Continuous Wave radar Ancortek (SDR 980AD2). It is followed by a pre-processing chain composed of Fast Fourier Transform, Filter and Short Time Fourier Transform to obtain time-velocity maps or spectrograms allowing the extraction of features from gait and human activities. An encouraging implementation (using SIMD logic) on Jetson Xavier allows us to move to data stream processing. Continuous monitoring of the subject will save lives, minimize injuries, reduce anxiety and prevent post-fall syndrome (PDS).

**Index Terms**—Elderly Fall Detection, Gait analysis, Human Activity Recognition, Radar, Real-Time

## I. INTRODUCTION

Nowadays, one global challenge is the rapid aging of the population. With an increasing dependency rate of 2% per year, France and its 1.1 million dependent and precarious people will not have the necessary health infrastructures to face the demographic and epidemiological transformation by 2040 [1]. The various health organizations, such as the World Health Organization (WHO), Santé Publique France (Public Health France), agree on this health hazard and the need to implement an early and targeted screening policy. Keeping elderly people independent for longer at home [2] is essential. Depending on the frailty of the patient [3], a personalized follow-up health course combined with feedback to clinicians has shown a reduction in the rate of falls, including those resulting in injury [4]. Therefore, fall risk should be assessed by estimating frailty and detecting near falls, which are multifactorial [3], [5], [6].

To be able to keep people with loss of autonomy at home for as long as possible and to respond to the policy of early and targeted screening recommended by the WHO, it is necessary to create human activity monitoring systems making it possible to detect and recognize human gait and more particularly falls and its warning signs. To help the patients stay at home,

systems must be real-time for a rapid intervention of first responders. Continuous monitoring of the subject will save lives, minimize injuries, reduce anxiety and prevent post-fall syndrome (PDS).

There are many sensors and associated systems in the literature for assisted living. The systems can be classified into different categories, one of the most common system classification being focused on the type of sensor used. They are categorized as follows Vision Based, Ambient Based, Wearables Based. A system that can combine several types of sensors falls into the network of sensors category. The wealth of information of the vision-based systems combined with the democratization of the Kinect which is an affordable, rather efficient and reliable using RGB and Depth (RGBD) technology allowing the posture of a person to be monitored for research in human activity recognition [7]. Wearables, based on inertial units, due to the democratization of smartphones, connected watches and the low cost of these sensors, there are many systems based on this technology [8], [9]. Finally there are ambient-based sensors which provide another mean of non-intrusive fall detection. Sensors such as active infrared, radio frequency identification (RFID), pressure, radar, ultrasonic and/or microphone are used to detect environmental changes due to the fall or activity [10]–[13].

There are several problems/shortcomings with these systems. There is a compromise to be found between the richness of information collected and the perceived privacy of the user [14]. an infrared sensor for example will not be perceived as invading privacy but will provide very coarse information whereas a visible camera will provide very rich information but be perceived as very invasive. Most people nowadays have accepted Wi-Fi routers in their homes. Therefore, the use of RF sensing with radar will be more easily accepted by end users. Additionally, if the data is leaked or hacked, it is not easily interpretable which increases security and reduces privacy concerns further. The radar being able to answer this problem but the quantity of data to be processed is very huge. The real-time detection of abnormal gait requires to implement a parallel software on a heterogeneous architecture.

In this paper we will therefore introduce a radar pre-processing architecture and its implementation on an embedded target, necessary for a future real-time system deployment. This pre-processing block will be integrated into a larger processing chain capable of recognizing and classifying human activity in real time while preserving the privacy of the information.

The rest of this article is organized as follows: The methodology of the architecture and its implementation is described in Section II, followed by some results of the implementation of the architecture with an analysis of these results is given in Section III. The last section is devoted to the conclusion.

## II. METHODOLOGY

### A. Radar data collection

In this study, the radar is an off-the-shelf Frequency Modulated Continuous Wave (FMCW) system from Ancortek (SDR 980AD2). It is operating in X-band with a carrier frequency of 9.8 GHz, with an instantaneous bandwidth equal to 400 MHz and a chirp duration equal to 1 ms. This yields an unambiguous Doppler frequency range equal to  $\pm 500$  Hz, which is sufficient to capture human activities performed indoors. The transmitted power was set to +19 dBm, two receiving antennas and one transmitting antenna were used. The architecture of the radar adopts a hardware dechirping/stretch processing for downconversion. This consists in mixing the received signal with the transmitted signal. The downconverted signal (beat frequency) is then low-pass filtered and amplified before digitization. There are 2 channels in the intermediate frequency after dechirping per receiving antenna one in-phase and the other in quadrature (*I/Q*) to capture complex samples. The sample rate is 128 per sweep (1ms), so the Analog to Digital Converter (ADC) sampling rate is 128 kHz.

### B. Pre-processing

Figure 1 presents the steps for the signal pre-processing chain. The processing starts with reading and reshaping the raw FMCW data in time domain. The raw data are the digitized reflected time-delayed and frequency-shifted signals in complex value before the application of Fast Fourier Transform (FFT) to extract the range information. In this project, the data matrix is reshaped into  $128 \times M$  where 128 is the number of time samples per sweep, which is also the size used for the FFT, and  $M$  is the number of chirps for an activity record. The FFT is applied to each column of the data matrix (i.e. each single chirp). The beat frequency is proportional to the time travelled by the signal from the transmitter to the target and back to the receiving antenna. The FFT on the raw data provides a range profile for each chirp. Next, a 9<sup>th</sup>-order high-pass Butterworth notch filter with a cutoff frequency at 0.0075 Hz is used as a moving target indicator to range profile, removing components near the 0 Hz in the frequency domain, which is caused by stationary objects in the environment. Only the range bins of interest from index 5 to 25 (which correspond to 1.875 m to 9.375 m) where the subjects are detected are chosen to reduce the noise for the generation of

the time velocity signature or spectrograms. A Short Time Fourier Transform (STFT) using a 0.2 s Hamming window with a 95% overlapping factor is implemented on the acquired range-time data matrix for each range bin in the slow time direction to extract the time-varying micro-Doppler signatures. For FMCW radar systems, the range resolution is proportional to the bandwidth. The Doppler resolution is related to both chirp duration and the number of FFT points used in the STFT. The range resolution is 37.5 cm, and the Doppler resolution is 1.25 Hz (or 0.03 m/s), making it capable of performing accurate velocity analysis and activity classification.

### C. Extraction features and further processing

This section describes the proposed approaches for feature extraction, which was not yet implemented but gives some context of the global processing chain, applied to both scenarios of monitoring human activities and gait analysis of specific individuals. For monitoring human activities we extract some features all along the pre-processing chain (from the data matrices) such as: the entropy, the energy curve (mean and variance), the centroid (mean and variance), the bandwidth [15]–[17]. For gait analysis of specific individuals some further processing from the micro-doppler spectrogram is needed to extract the velocity-time and acceleration-time graph. From where you can extract several useful features (velocity of the the stable phase, step repetition frequency, step length) [11].

### D. Target implementation device

To embed the future processing/classification chain into a power efficient yet powerful complete system (Central Processing Unit (CPU) + Graphics Processing Unit (GPU) + Hardware Accelerators), we implemented the pre-processing chain into two different architectures then performed some benchmarks on an Nvidia JETSON TX2 and an Nvidia Jetson Xavier. The main characteristics of the JETSON TX2 are as follows:

- CPU: One HMP (6 cores) including 2 Denver cores (custom core designed by Nvidia to run the ARMv8 ISA) and 4 ARM Cortex A57 cores (to run the ARMv8 ISA). All the cores are compatible with ISA ARMv8 which is the 64-bit architecture of ARM.
- Random Access Memory (RAM): 8 GB L128 bit DDR4 of main memory.
- GPU: A Pascal GPU at 256 CUDA cores (not used in this experiment).
- Operating System (OS) : It runs on L4T (Linux for Tegra, i.e. Linux Kernel 4.9).

The main characteristics of the JETSON Xavier are as follows:

- CPU: One HMP of 4 x 2 cores of Carmel cores (custom core designed by Nvidia to run the ARMv8.2 ISA).
- RAM: 32 GB 256-bit LPDDR4x of main memory.
- GPU: A Volta GPU at 512 CUDA cores (not used in this experiment).
- OS : It runs on L4T (Linux for Tegra, i.e. Linux Kernel 4.9).



Fig. 1. Pre-processing chain

### E. Implementation architecture

As we had to apply the same processing to the data from both receiving antennas (1 & 2), we applied multi-core Single Instruction on Multiple Data (SIMD) logic with OpenMP, after a first phase of extracting and shaping the I/Q radar data, the same processing described in the pre-processing part had been executed, one CPU core (Carmel) per receiving channel. The Discrete Fourier Transforms (DFTs) were performed with the FFTW3 library, the STFT block was custom coded making use of FFTW3 and the implementation of the Butterworth high pass filter was a 9<sup>th</sup> Butterworth high pass filter for which the coefficients were pre-calculated in Matlab.

## III. RESULTS & DISCUSSIONS

We had to be able to compare processing performances in view of the transition from data packet processing to data stream processing. We chose the execution time as a comparison value in the benchmarks that were performed. Figure 2 depicts the execution time of a 10s activity record for the following benchmarks. All implementations were coded in C++.

The first implementation architecture [A] was a single-core on the Jetson TX2, running the code exclusively on the Denver core (at a frequency of 1.4 GHz). We then ported the implementation to the Jetson Xavier by executing the code on a Carmel core at a frequency of 1.2 GHz [B], we obtain a first step in the acceleration of the code due to the intrinsic architecture of the CPU, the Denver being a 7 Way Superscalar CPU and the Carmel being a 10 Way Superscalar CPU. To continue the acceleration, we increased the operating frequencies of the Jetson TX2 up to 2.0 GHz, by executing the code exclusively on the Denver core [C] and then on the Cortex A57 core [D], we obtain a second step in the acceleration. We then switched to the dual-core OpenMP, since previously, we were sequentially performing the same processing on the data from antenna 1 then on the data from antenna 2. By following an SIMD logic, we went from a processing time of 17.1s time in a single core Carmel [B] to a processing of 9.6s [E]. Finally we increased the operating frequency of the Jetson Xavier up to 2.2 GHz allowing us to reach an execution time of 4.8s on a Carmel core [F] (the maximum frequency of the Jetson Xavier being higher than the Jetson Tx2 due to the engraving technology). We have achieved an acceleration factor of 3.56 compared to our first implementation.

Figure 3 represents the output of the pre-processing chain, it is the micro-Doppler spectrogram with a colorbar indicating the spectrogram amplitudes (calculated by squaring the output matriux after the STFT). From the spectrogram, we will then be able to extract some features as explained in Section II for

human activity recognition and/or gait analysis.

TABLE I  
DESCRIPTION OF THE TESTS

	Platform, number of cores, frequency, platform
A	Denver Nvidia, single core, 1.4 GHz, Jetson TX2
B	Carmel Nvidia, single core, 1.2 GHz, Jetson Xavier
C	Denver Nvidia, single core, 2.0 GHz, Jetson TX2
D	ARM Cortex A57, single core, 2.0 GHz, Jetson TX2
E	Carmel Nvidia, dual core, 1.2 GHz, Jetson Xavier
F	Carmel Nvidia, dual core, 2.2 GHz, Jetson Xavier

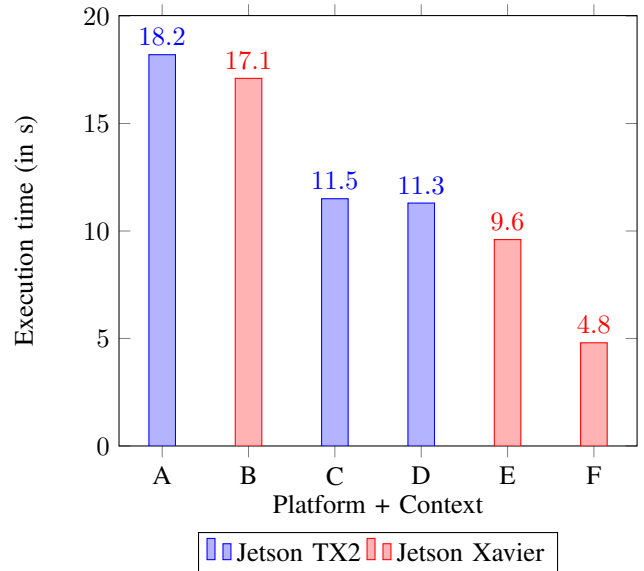


Fig. 2. Processing execution time according to different embedded platforms and different architectures

From the results on both platforms, we have reached a performance level allowing us to move to stream processing architectures. The dual core solution at the frequency of 2.2 GHz on the Carmel CPUs of the Jetson Xavier executes the processing of a 10s radar recording sufficiently quickly which allows us to consider the use of Direct Memory Access (DMA) to manage the acquisition and storage of data in pre-processing buffers. There is still room for improvement in the optimization of the pre-processing chain, such as the use of the GPU for the FFTs, best Cache Memory utilization and organization of the data in memory to further improve performances.

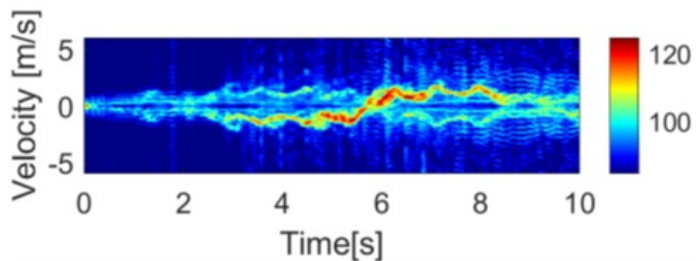


Fig. 3. Micro-Doppler spectrogram of a 10s record

#### IV. CONCLUSION

In conclusion, in this article we proposed an embedded implementation of the pre-processing chain before feature extraction towards the real-time classification of fall risk of a person and the detection of falls. We have introduced an architecture (dual core SIMD approach) with timings allowing us to move from data packet architectures to data stream architecture for further implementations. Thanks to these encouraging results, we soon plan to switch the architecture to data flows and introduce the future blocks of the processing chains (feature extractions and classification).

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